



2183
PATENT 41

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Dally et al.	Examiner:	Meonske, T.
Serial No.:	09/871,301	Group Art Unit:	2183
Filed:	May 30, 2001	Docket No.:	STFD.076CIP
Title:	System And Method For Performing Efficient Conditional Vector Operations For Data Parallel Architectures Involving Both Input and Conditional Vector Values		

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence and the papers, as described hereinabove, are being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 4, 2004.

By: *E. M. Nichols*
Erin M. Nichols

OFFICE ACTION RESPONSE AND AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Technology Center 2100

Dear Sir:

In response to the non-final Office Action dated May 4, 2004, please consider the following amendment and remarks.

Amendments to the title begin on page 2, amendments to the claims are reflected in the listing of claims beginning on page 3 and the Remarks begin on page 15.

In the Title:

Please amend the title as indicated below.

System And Method For Performing Efficient Conditional Vector Operations For Data
Parallel Architectures Involving Both Input And Conditional Vector Values